

FIGURE 1

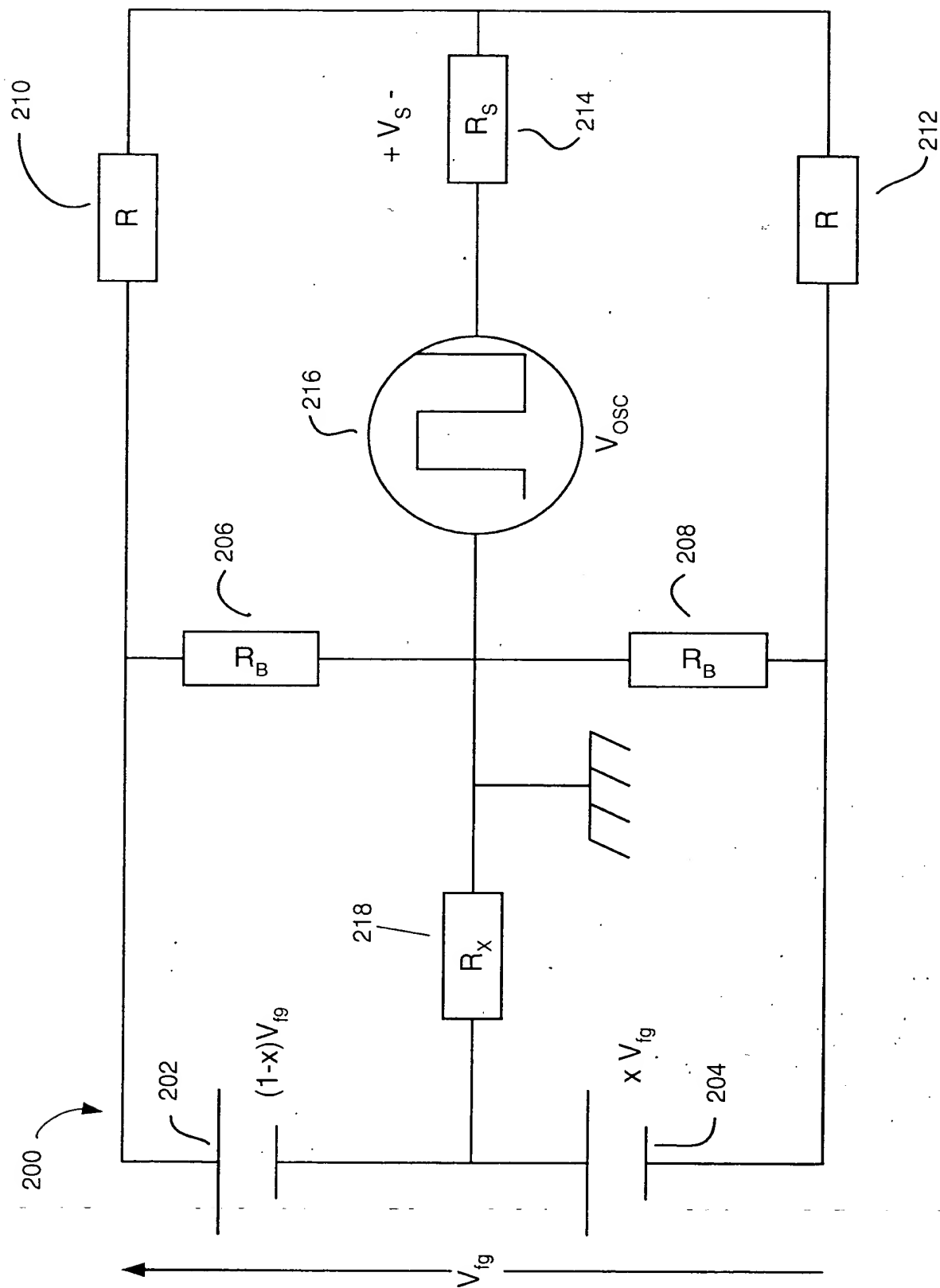


FIGURE 2

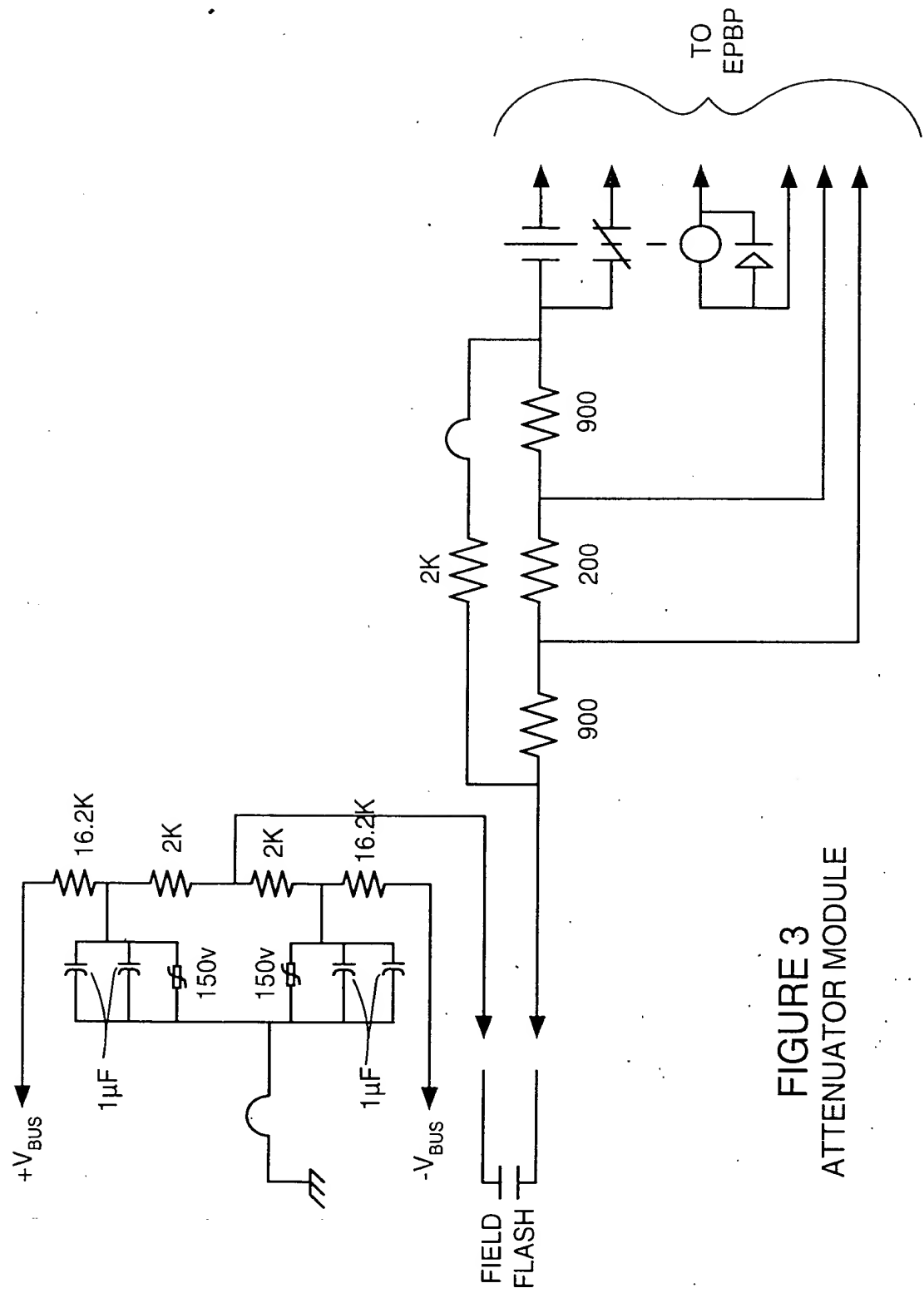


FIGURE 3
ATTENUATOR MODULE

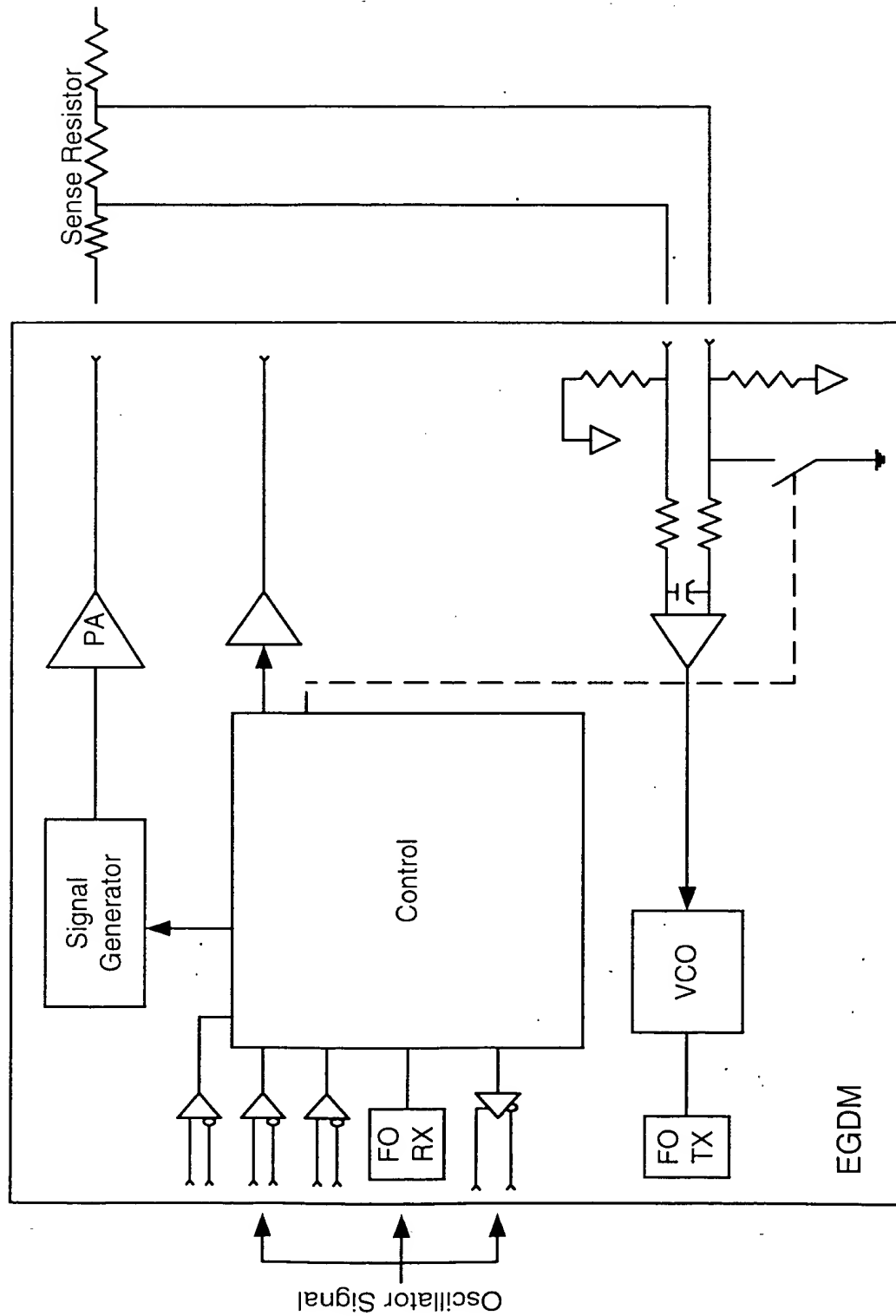


FIGURE 4

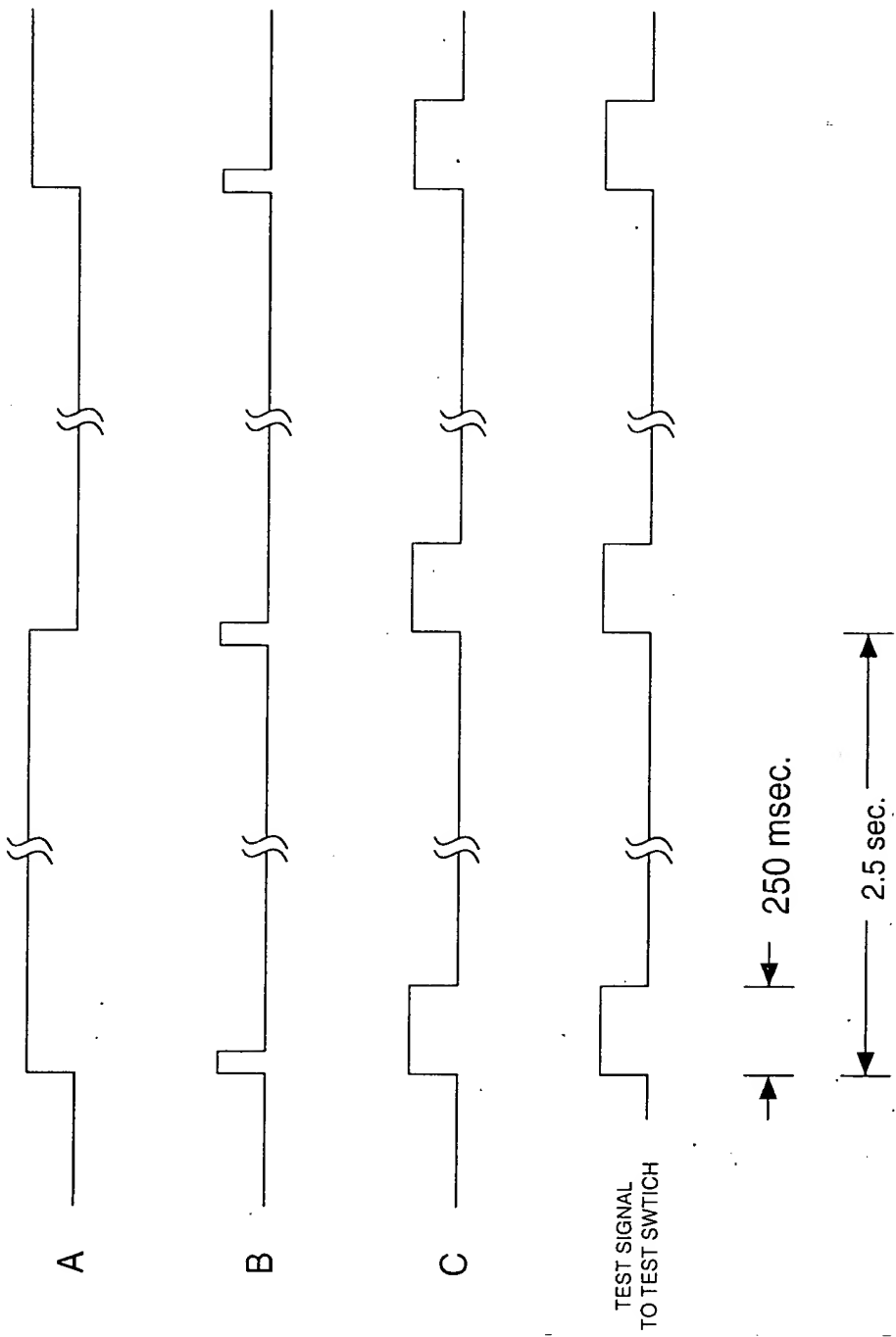


FIGURE 6
TIMING DIAGRAM

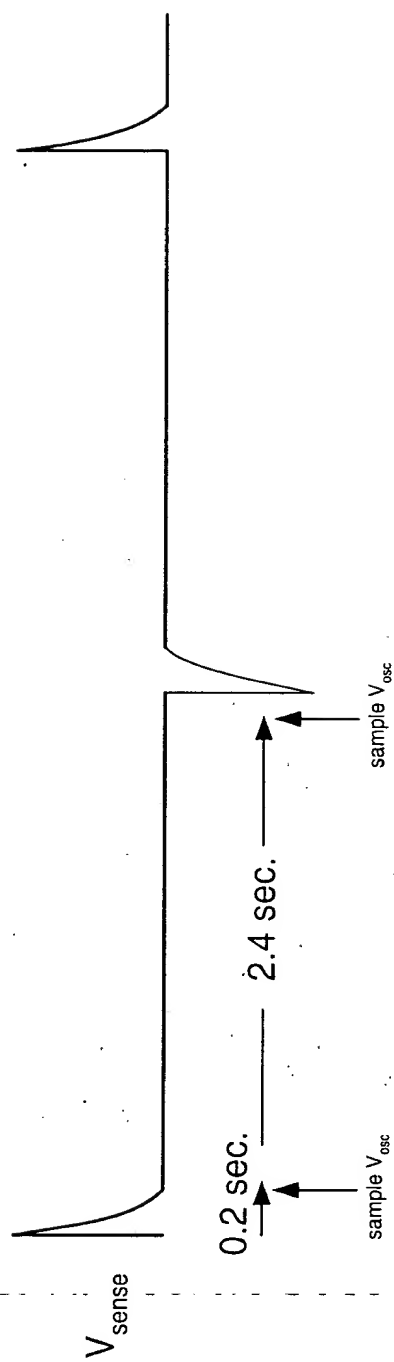
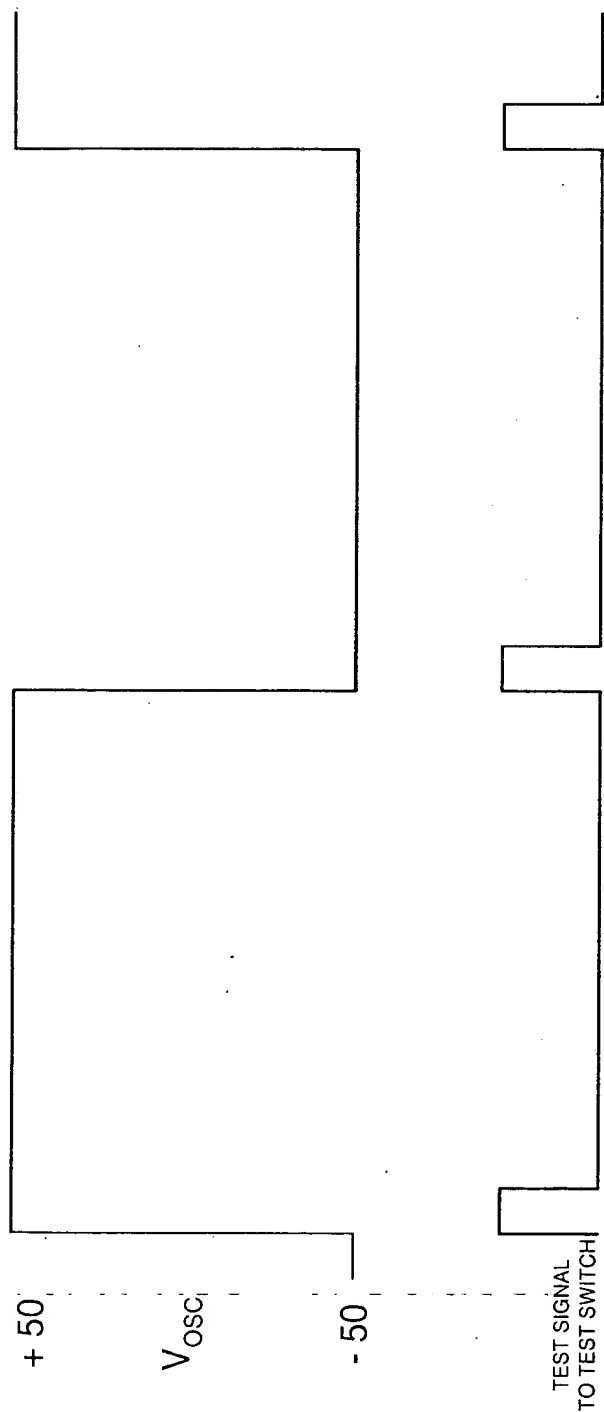


FIGURE 7

FIGURE 8
TIMING DIAGRAM FOR TEST MODE OPERATION

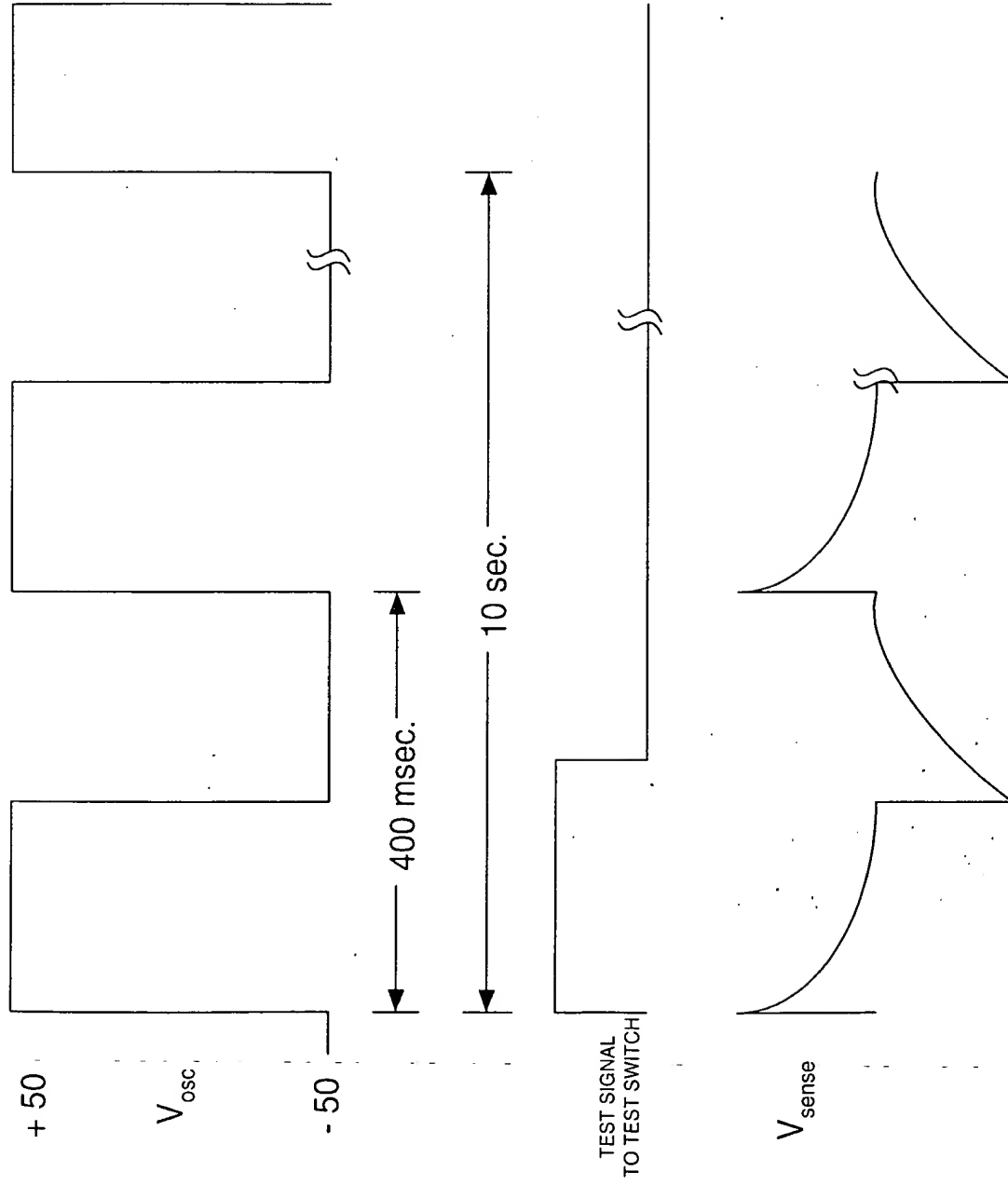


FIGURE 9
OPERATION OF FIELD GROUND DETECTOR DURING TEST MODE

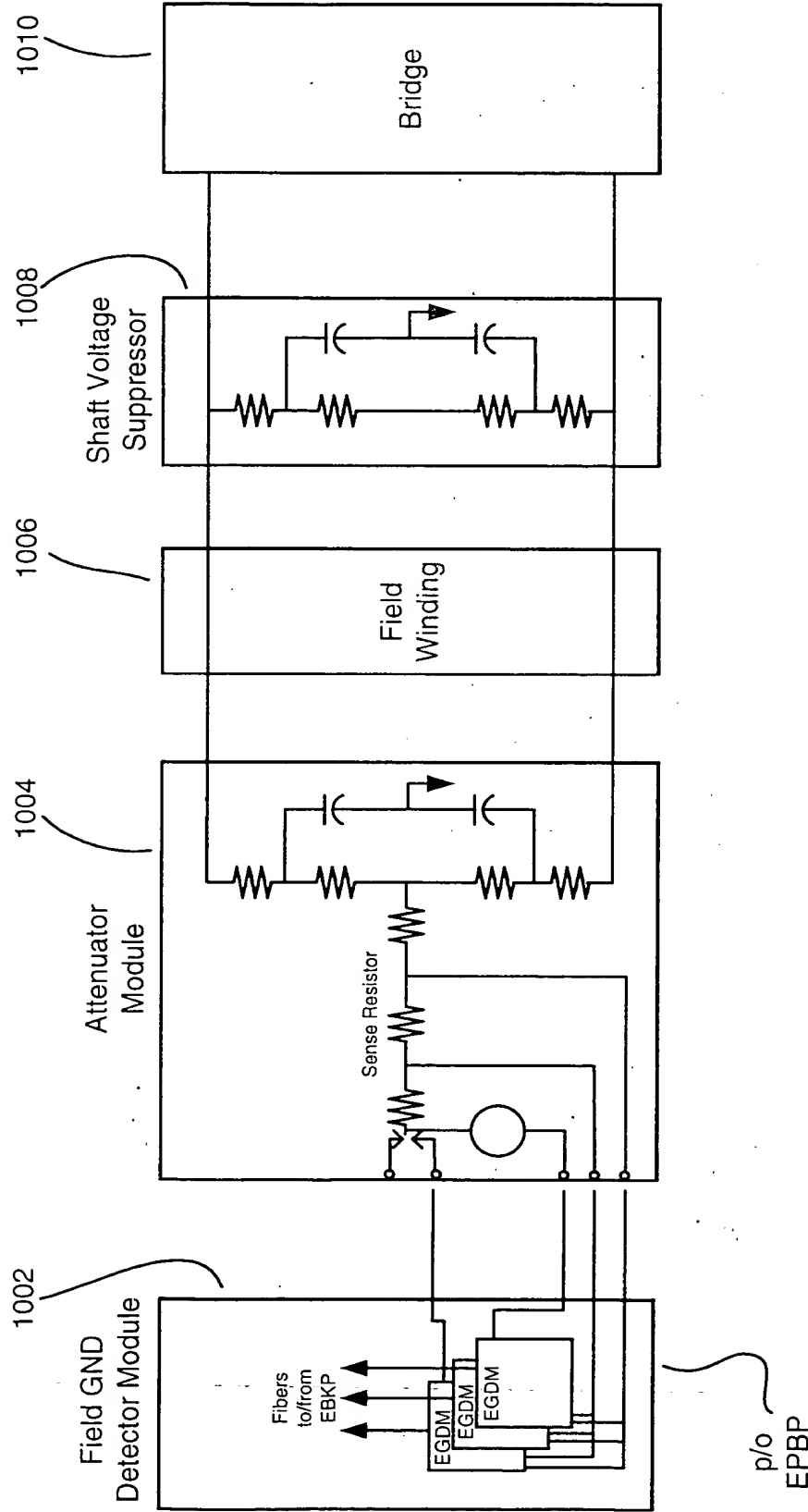


FIGURE 10

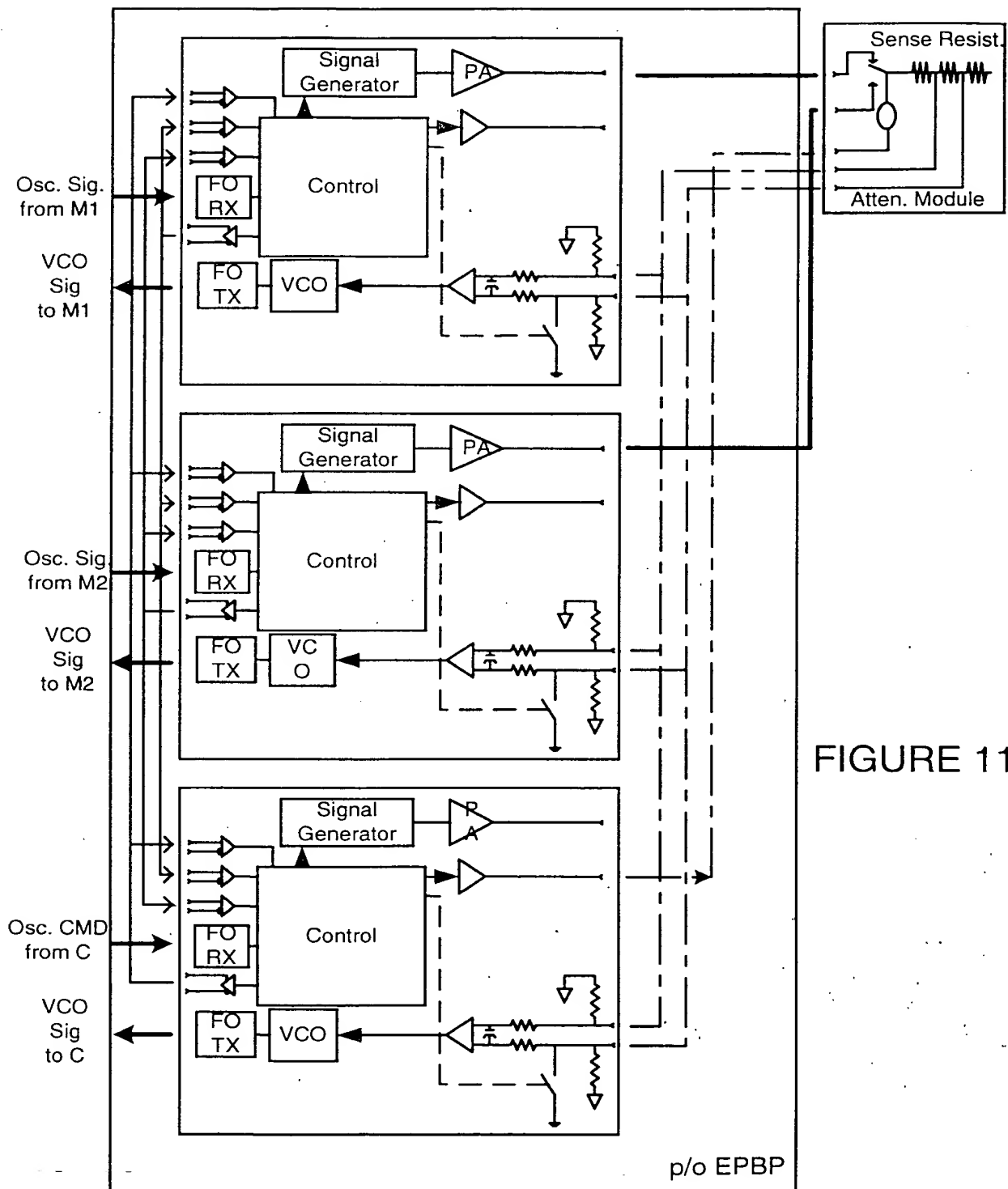


FIGURE 11

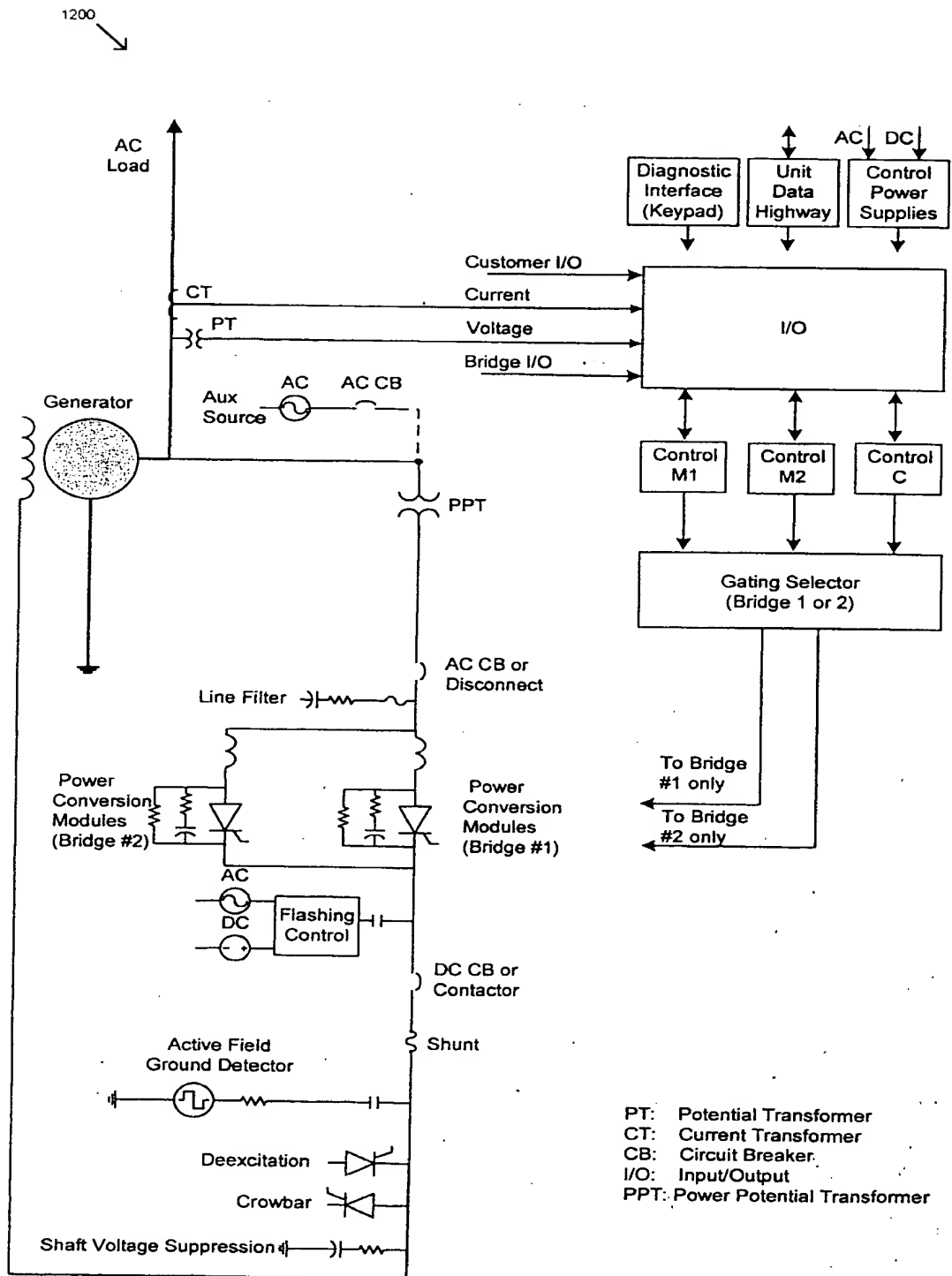


Figure 12